



ASI-T-430FA2AT/AH

Item	Display Panel	Remark
Display Mode	Normally White, Transmissive LCD	
Viewing Direction	12 O'CLOCK	'HJUHHV DW 'HJUHHV DW
Input Signals	RGB	
Outside Dimensions	105.5mm(W)*67.2mm(H)*3.0mm(T)	
Effective Area	-	
Active Area	95.04mm(W)×53.856mm(H)	
Number of Pixels	480×RGB×272Pixels	
Pixel Pitch	0.198mm(H) × 0.198mm(W)	
Pixel Arrangement	RGB Vertical stripes	
Drive IC	ST7282	

R FORFN
R FORFN



CONTENTS

- GENERAL DESCRIPTION
- GENERAL FEATURES
- ABSOLUTE MAXIMUM RATINGS
- ELECTRICAL SPECIFICATIONS
- OPTICAL SPECIFICATIONS
- BLOCK DIAGRAM
- PIN DESCRIPTION
- OUTLINE DIMENSION
- REGISTER VALUE
- TIMING CHARACTERISTICS
- RELIABILITY AND INSPECTION STANDARD
- INSPECTION CRITERION
- PACKING DIMENSION
- PRECAUTIONS



1. General Description

This main Module has a **4.3** inch diagonally measured active display area with 480(RGB)X272 resolution. Each pixel is divided into Red, Green and Blue sub-pixels and dots which are arranged in vertical stripes.

LCD color is determined with 262,000 colors signal for each pixel.

The ASI-T-430FA2AT/AH has been designed to apply the interface method that enables low power, high speed, and high contrast.

The ASI-T-430FA2AT/AH is intended to support applications where thin thickness, wide viewing angle and low power are critical factors and graphic displays are important.

2. General Features

Item	Display Panel	Remark
Display Mode	Normally White, Transmissive LCD	
Viewing Direction	12 O'CLOCK	'HJUHHV DW 'HJUHHV DW
Input Signals	RGB	
Outside Dimensions	105.5mm(W)*67.2mm(H)*3.0mm(T)	
Effective Area	-	
Active Area	95.04mm(W)×53.856mm(H)	
Number of Pixels	480×RGB×272Pixels	
Pixel Pitch	0.198mm(H) ×0.198mm(W)	
Pixel Arrangement	RGB Vertical stripes	
Drive IC	ST7282	

R FORFN
R FORFN

3. Absolute Maximum Ratings

The following are maximum values which, if exceeded may cause operation or damage to the unit.

ITEM	Symbol	Min.	Typ.	Max.	Unit	Remark
Power for Circuit Driving	VDD		3.3		V	
Power for Circuit Logic	VCI		3.3		V	
LC Operating Voltage *1)	Vop		3.3		V	
LED Forward Voltage	V _f	-	22.4	-	V	
LED Forward Current	I _r	-	20	-	mA	
LCD Luminance	B _P	-	250	-	cd/m ²	
Storage Humidity	H _{ST}	10	-	90	%RH	At 25±5°C
Storage Temperature	T _{ST}	-30	-	80	°C	
Operating Ambient Humidity	H _{OP}	10	-	90	%RH	
Operating Ambient temperature	T _{OP}	-20	-	70	°C	

Note:

- *1) Liquid Crystal driving voltage.
Due to the characteristics of LC Material, this voltage vary with environmental temperature.
- *2) Temp. >60°C, Absolute humidity shall be less than 90%RH at 60°C
- *3) Temp. ≤60°C, 90%RH MAX.

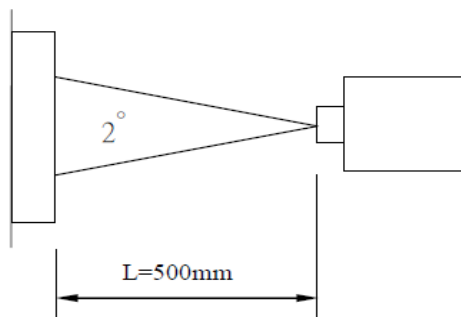
4. Optical Specification

(Transmittance · contrast · RT · viewing angle results are using CPT LC+ EWV Polarizer+ CPT's BLU (2L1D) reference only) (Note1 · Note2)

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
Transmittance	T		5.6	5.9		%	Note 2
Contrast Ratio	CR	*1)	250	350	--	--	Note 3
Response Time	Tr+ Tf	*3)	-	30	45	ms	Note 4
Viewing Angle	Vertical	θ *2)	90	110	--		Note 5
	Horizontal	ϕ *2)			110	130	
Color Filter Chromacicity with C light	White	x	$\theta = \phi = 0^\circ$	0.282	0.302	0.322	Note 6
		y		0.318	0.338	0.358	
	Red	x	$\theta = \phi = 0^\circ$	0.586	0.606	0.626	
		y		0.305	0.325	0.345	
	Green	x	$\theta = \phi = 0^\circ$	0.283	0.303	0.323	
		y		0.547	0.567	0.587	
Blue	x	$\theta = \phi = 0^\circ$	0.127	0.147	0.167		
	y		0.121	0.161	0.181		
NTSC			-	50%	-		

Note 1. Ambient condition : 25°C ±2°C · 60±10%RH · under 10 Lux in the darkroom ◦

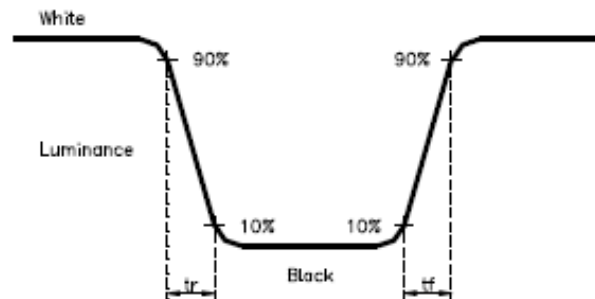
Note 2. Measure device : BM-5A (TOPCON) · viewing cone=2° · I_L=20mA ◦



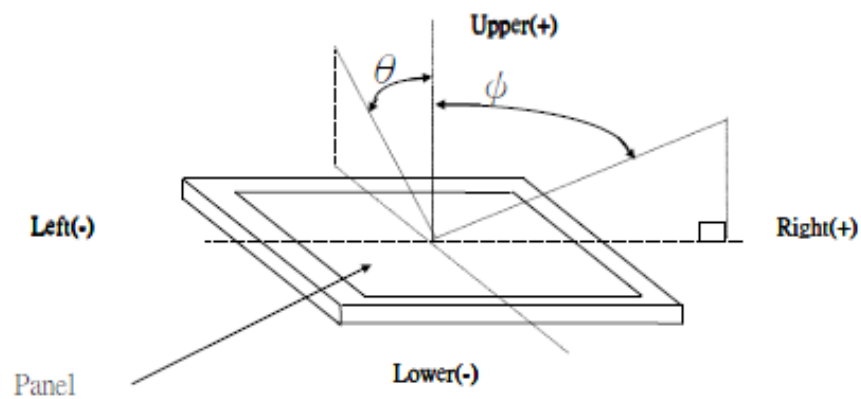
Note 3. Definition of Contrast Ratio :

$$CR = \text{White Luminance (ON)} / \text{Black Luminance (OFF)}$$

Note 4. Definition of response time : The response time is defined as the time interval between the 10% and 90% amplitudes.

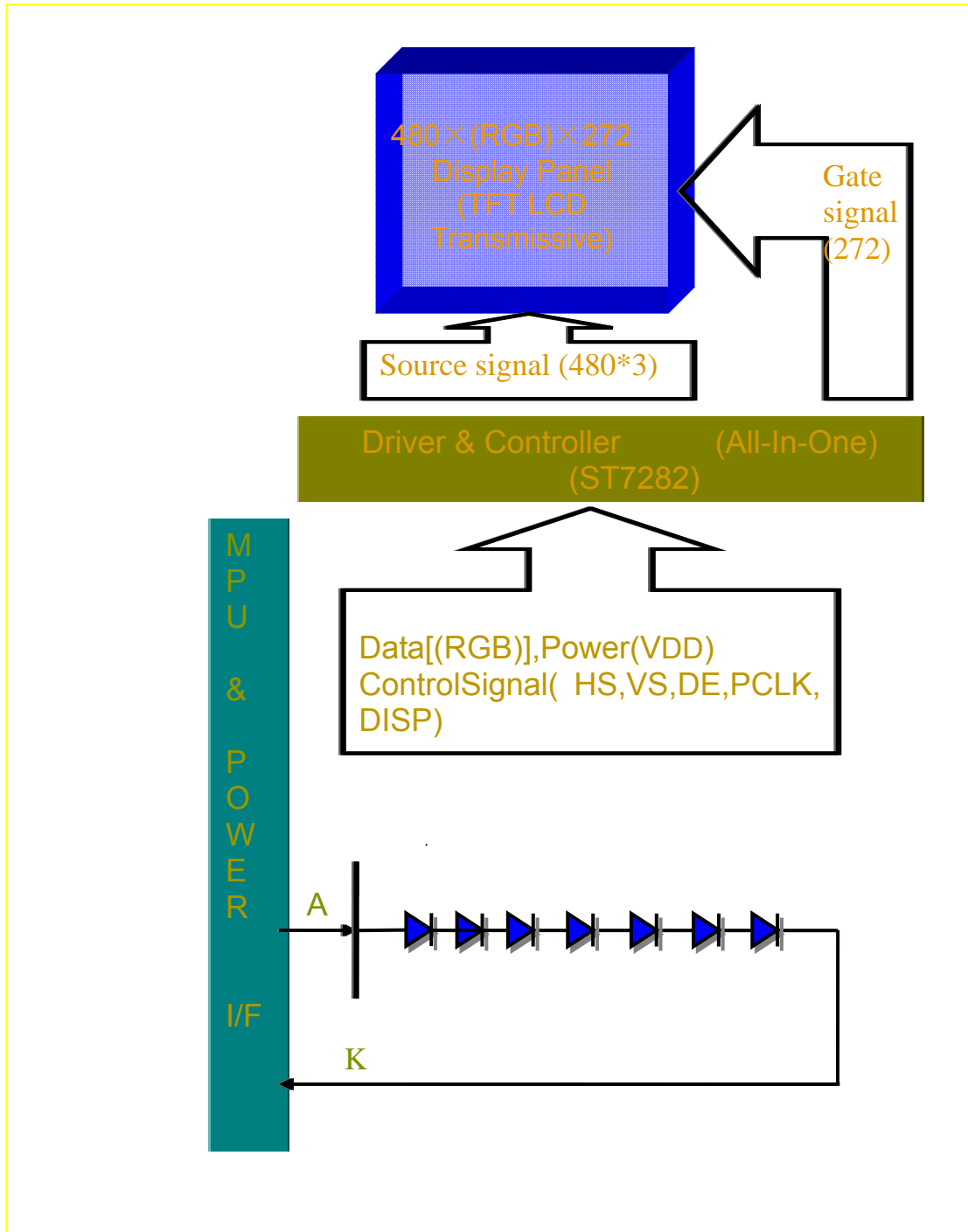


Note 5. Definition of view angle(θ , ψ) :



Note 6. Light source: C light.

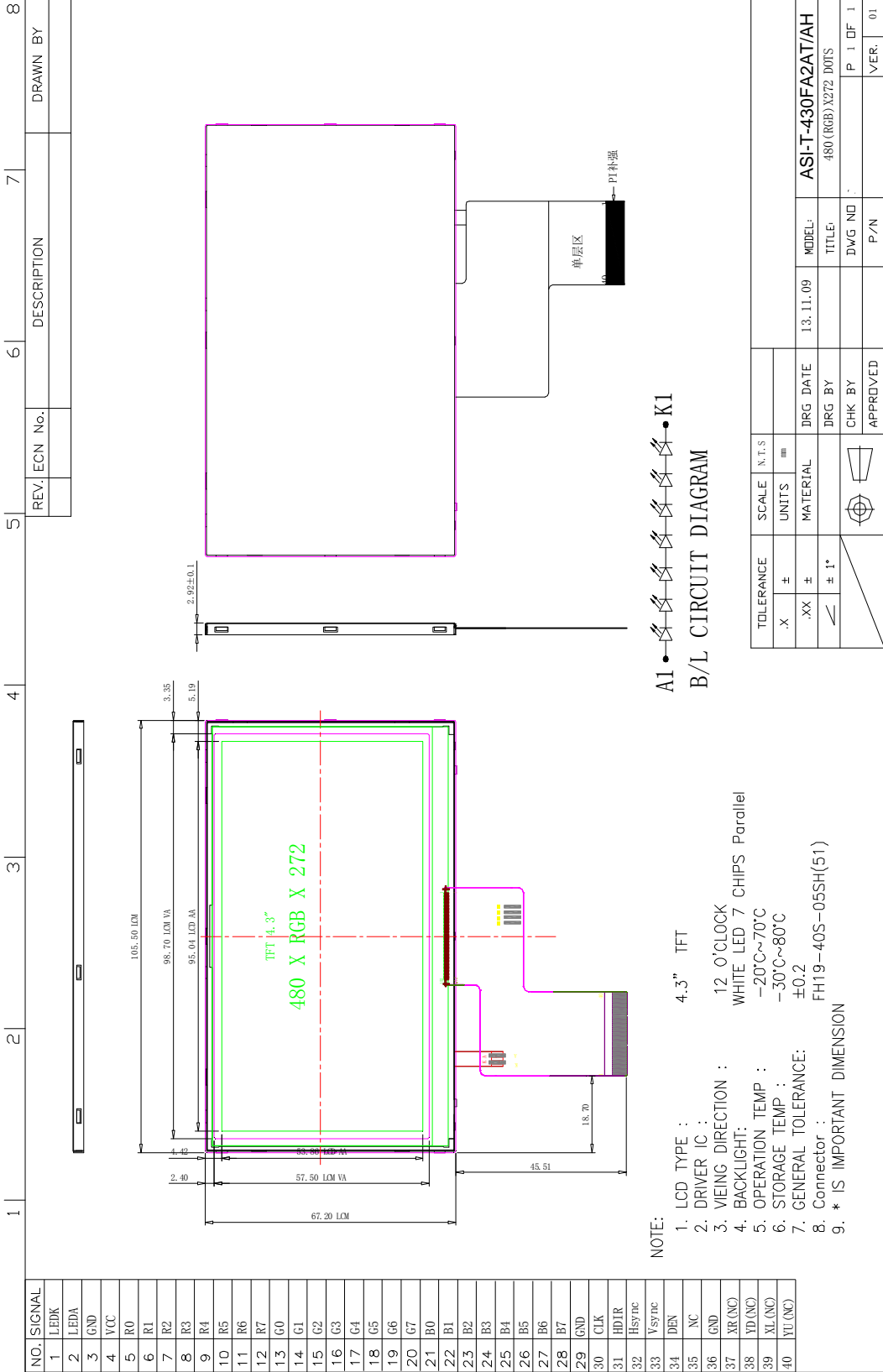
5. Block Diagram



6. Pin Description

Pin NO.	Symbol	Description
1	LEDK	LED BACKLIGHT(CATHODE)
2	LEDA	LED BACKLIGHT(ANODE)
3	GND	GROUND
4	VCC	POWER SUPPLY
5	R0	RED DATA
6	R1	RED DATA
7	R2	RED DATA
8	R3	RED DATA
9	R4	RED DATA
10	R5	RED DATA
11	R6	RED DATA
12	R7	RED DATA
13	G0	GREEN DATA
14	G1	GREEN DATA
15	G2	GREEN DATA
16	G3	GREEN DATA
17	G4	GREEN DATA
18	G5	GREEN DATA
19	G6	GREEN DATA
20	G7	GREEN DATA
21	B0	BLUE DATA
22	B1	BLUE DATA
23	B2	BLUE DATA
24	B3	BLUE DATA
25	B4	BLUE DATA
26	B5	BLUE DATA
27	B6	BLUE DATA
28	B7	BLUE DATA
29	GND	GROUND
30	CLK	CLOCK SIGNAL
31	HDIR	Shift from left or right
32	HSYNC	HORIZONTAL SYNC INPUT IN RGB MODE
33	VSYNC	VERTICAL SYNC INPUT IN RGB MODE
34	DEN	DATA ENABLE
35	NC	NC
36	GND	GROUND
37	XR(NC)	TOUCH PLANE PIN/NC
38	YD(NC)	TOUCH PLANE PIN/NC
39	XL(NC)	TOUCH PLANE PIN/NC
40	YU(NC)	TOUCH PLANE PIN/NC

8. Outline Dimension



A1 • • • • • K1
B/L CIRCUIT DIAGRAM

- NOTE:
1. LCD TYPE : 4.3" TFT
 2. DRIVER IC : 12 0'CLOCK
 3. VIEWING DIRECTION : WHITE LED 7 CHIPS Parallel
 4. BACKLIGHT :
 5. OPERATION TEMP : -20°C~70°C
 6. STORAGE TEMP : -50°C~80°C
 7. GENERAL TOLERANCE: ±0.2
 8. Connector : FH19-40S-05SH(51)
 9. * IS IMPORTANT DIMENSION

TOLERANCE	SCALE	N.T.S	DRG DATE	MODEL
.X ±	UNITS	mm	13.11.09	ASI-T-430FA2AT/AH
.XX ±	MATERIAL		DRG BY	TITLE
← ± 1°			CHK BY	480 (RGB) X272 DOTS
			APPROVED	DWG NO
				P / N
				VER.
				01

9. Timing Characteristics

Item	Symbol	Min.	Typ.	Max.	Unit	Remark	
DCLK Frequency	Fclk	9	12	15	MHz		
DCLK Period	Tclk	67	83	111	ns		
HSYNC	Period Time	Th	486	526	533	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	50	DCLK	By H_Blanking setting
	Front Porch	Thfp	2	2	2	DCLK	
	Pulse Width	Thw	1	1	1	DCLK	
VSYNC	Period Time	Tv	276	286	304	H	
	Display Period	Tvdisp		272		H	
	Back Porch	Tvbp	2	12	30	H	By V_Blanking setting
	Front Porch	Tvfp	1	1	1	H	
	Pulse Width	Tvw	1	1	1	H	

9.2. Timing Characteristics

Normal Write Mode(HWM='0'), IOVcc=1.65V~3.6V,Vcc=2.5V~3.6V

Parameter	Symbol	Unit	Min.	Max.	Unit
Bus cycle time write	t _{CYCW}	ns	100	-	-
Bus cycle time read	t _{CYCR}	ns	160	-	-
Write low-level pulse width	PW _{LW}	ns	35	-	-
Read low-level pulse width	PW _{LR}	ns	45	-	-
Write high-level pulse width	PW _{HW}	ns	35	-	-
Read high-level pulse width	PW _{HR}	ns	90	-	-
Write/Read rise/fall time	t _{WR,WRf}	ns	-	-	25
Setup time Write	t _{AS}	ns	0	-	-
Setup time Read	t _{AS}	ns	10	-	-
Address hold time	t _{AH}	ns	2	-	-
Write data setup time	t _{DSW}	ns	25	-	-
Write data hold time	t _H	ns	5	-	-
Read data delay time	t _{DDR}	ns	-	-	100
Read data hold time	t _{DHR}	ns	5	-	-

9.3 Reset Operation

(VCC=1.65~3.1 V)

Table 13-6

Item	Symbol	Unit	Min.	Typ.	Max.
Reset low-level width	tRES	ms	1	—	—
Reset rise time	trRES	μs	—	—	10

10. Reliability and Inspection Standard

No.	Test Item	Test Conditions	Remark	
1	High Temperature	Storage	70°C, 120Hr	Note
		Operation	60°C, 120Hr	Note
2	Low Temperature	Storage	-30°C, 120Hr	Note
		Operation	-20°C, 120Hr	
3	High Temperature and High Humidity	60°C, 90%RH, 120Hr	Note	
4	Temperature Cycle	Storage	-10°C(1Hr)→25°C(5min)→60°C(1Hr) 32 Cycles	Note
		Operation	-20°C(1Hr)→25°C(5min)→60°C(1Hr) 25 Cycles	
5	Peeling Off (Storage)	≥ 500gf/cm	Note	
6	FPC Bending Test	≥ 6,000 times, 2/sec	Note	
7	Vibration Test(Storage)	50HZ, 30min, Amplitude: 2 cm, X/Y/Z directions	Note	
8	Drop Test	60cm/ 3Corner/ 8Face, 1Cycle	Note	
9	Electrostatic Discharge	+/-200V, 200pf(0ohm) 1 time/each terminal		

Note:

- 1) The test samples should be applied to only one test item.
- 2) Sample size for each test item is 5~10pcs.
- 3) For Damp Proof Test, pure water(Resistance>1MΩ) should be used.
- 4) In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5) EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and fluorescence EL has.
- 6) After the reliability test, the test samples should be inspected after 2 hours at least.
- 7) Functional test is OK. Missing segment, shorts, unclear segment, non display, display abnormally, liquid crystal leak are not allowed.
- 8) After testing, the current Idd should be within initial value ±20%.
- 9) No low temperature bubbles ,end seal loose and fall, frame rainbow, ACF bubble growing are allowable in the appearance test.

11. Inspection Criterion

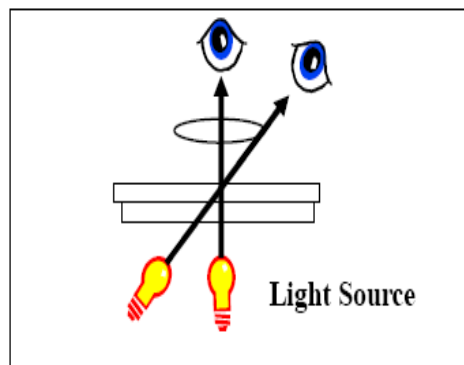
11.1. Sampling Method

Unless otherwise agreed upon in writing, the sampling inspection shall be applied to the Customer's incoming inspection.

- 1) Lot size: Quantity per shipment lot
- 2) Sampling type: Normal inspection , single sampling
- 3) Inspection level: II
- 4) Sampling table: MIL-STD-105D
- 5) Acceptable Quality Level(AQL): Major=0.65 Minor=1.5

11.2. Inspection Method

- 1) Ambient Condition:
 - a. Temperature: Room temperature $25\pm 5^{\circ}\text{C}$
 - b. Illumination: Single fluorescent lamp non-directive(300 to 700 Lux)
- 2) Viewing distance
The distance between the LCD and the inspector' s eyes shall be at least 30-50cm.
- 3) Viewing Angle
The inspection shall be conducted within normal viewing angle range.



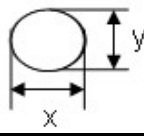

12.3. Inspection Criteria

12.3.1. Major defect

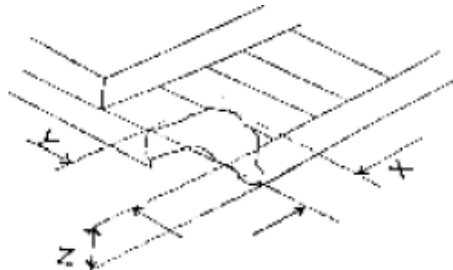
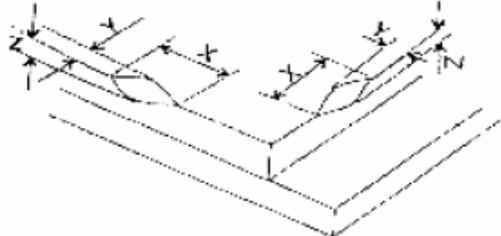
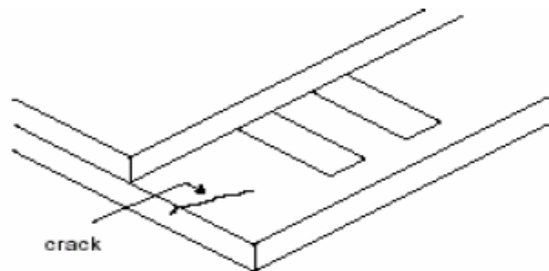
No.	Item	Inspection Standard	Classification of defects
1	All functional defects	1) No display 2) Display abnormally 3) Open or missing segment 4) Short circuit 5) Excess power consumption 6) Backlight no lighting, flickering and abnormal lighting	Major
2	Missing	Missing component	Major

3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	Major
---	-------------------	---	-------

12.3.2. Cosmetic Defect

No.	Item	Inspection Standard	Classification of defects										
1	(spot defect) Black and White spot pinhole	<p>For dark/white spot, size Φ is defined as $\Phi=(x+y)/2$</p>  <table border="1" style="margin-top: 10px;"> <thead> <tr> <th style="text-align: center;">Size Φ (mm)</th> <th style="text-align: center;">Acceptable Quantity</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">$\Phi \leq 0.1$</td> <td style="text-align: center;">Ignore</td> </tr> <tr> <td style="text-align: center;">$0.10 \leq \Phi \leq 0.15$</td> <td style="text-align: center;">2</td> </tr> <tr> <td style="text-align: center;">$0.15 \leq \Phi \leq 0.2$</td> <td style="text-align: center;">1</td> </tr> <tr> <td style="text-align: center;">$0.2 < \Phi$</td> <td style="text-align: center;">0</td> </tr> </tbody> </table>	Size Φ (mm)	Acceptable Quantity	$\Phi \leq 0.1$	Ignore	$0.10 \leq \Phi \leq 0.15$	2	$0.15 \leq \Phi \leq 0.2$	1	$0.2 < \Phi$	0	Minor
Size Φ (mm)	Acceptable Quantity												
$\Phi \leq 0.1$	Ignore												
$0.10 \leq \Phi \leq 0.15$	2												
$0.15 \leq \Phi \leq 0.2$	1												
$0.2 < \Phi$	0												
2	(line defect) Black and White line Polarizer scratch	<p>Define:</p>  <table border="1" style="margin-top: 10px;"> <thead> <tr> <th style="text-align: center;">Width(mm)</th> <th style="text-align: center;">Length(mm); Acceptable Qty</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">$\Phi \leq 0.03$</td> <td style="text-align: center;">Ignore</td> </tr> <tr> <td style="text-align: center;">$0.03 < W \leq 0.05$</td> <td style="text-align: center;">$L \leq 3.0$; $N \leq 2$</td> </tr> <tr> <td style="text-align: center;">$0.05 < W \leq 0.1$</td> <td style="text-align: center;">$L \leq 2.0$; $N \leq 2$</td> </tr> <tr> <td style="text-align: center;">$0.1 < W$</td> <td style="text-align: center;">Define as spot defect</td> </tr> </tbody> </table>	Width(mm)	Length(mm); Acceptable Qty	$\Phi \leq 0.03$	Ignore	$0.03 < W \leq 0.05$	$L \leq 3.0$; $N \leq 2$	$0.05 < W \leq 0.1$	$L \leq 2.0$; $N \leq 2$	$0.1 < W$	Define as spot defect	Minor
Width(mm)	Length(mm); Acceptable Qty												
$\Phi \leq 0.03$	Ignore												
$0.03 < W \leq 0.05$	$L \leq 3.0$; $N \leq 2$												
$0.05 < W \leq 0.1$	$L \leq 2.0$; $N \leq 2$												
$0.1 < W$	Define as spot defect												
3	Polarizer defect	<p>Dent or bubble(between the polarizer and glass)</p> <table border="1" style="margin-top: 10px;"> <thead> <tr> <th style="text-align: center;">Size Φ(mm)</th> <th style="text-align: center;">Acceptable Qty</th> </tr> </thead> <tbody> <tr> <td style="text-align: center;">$\Phi \leq 0.10$</td> <td style="text-align: center;">Ignor</td> </tr> <tr> <td style="text-align: center;">$0.10 < \Phi \leq 0.20$</td> <td style="text-align: center;">2</td> </tr> <tr> <td style="text-align: center;">$0.20 < \Phi \leq 0.30$</td> <td style="text-align: center;">1</td> </tr> <tr> <td style="text-align: center;">$0.30 < \Phi$</td> <td style="text-align: center;">0</td> </tr> </tbody> </table>	Size Φ (mm)	Acceptable Qty	$\Phi \leq 0.10$	Ignor	$0.10 < \Phi \leq 0.20$	2	$0.20 < \Phi \leq 0.30$	1	$0.30 < \Phi$	0	Minor
Size Φ (mm)	Acceptable Qty												
$\Phi \leq 0.10$	Ignor												
$0.10 < \Phi \leq 0.20$	2												
$0.20 < \Phi \leq 0.30$	1												
$0.30 < \Phi$	0												

12.3.3. Cosmetic Defect

No.	Item	Inspection Standard	Classification of defects						
1	Glass defect	<p>1) Chip on the corner</p>  <table border="1" data-bbox="464 781 1098 878"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>≤3.0</td> <td>≤S</td> <td>≤T</td> </tr> </table> <p>Remark: S=contact pad length; T=the thickness of glass Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal. Acceptable Quantity N≤2.</p>	X	Y	Z	≤3.0	≤S	≤T	Minor
		X	Y	Z					
		≤3.0	≤S	≤T					
<p>2) Chip on the edge of glass</p>  <table border="1" data-bbox="464 1373 1098 1478"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>Ignore</td> <td>≤0.5</td> <td>≤T</td> </tr> </table> <p>Acceptable Quantity: N≤2</p>	X	Y	Z	Ignore	≤0.5	≤T	Minor		
X	Y	Z							
Ignore	≤0.5	≤T							
<p>3) Creak Creaks tend to break are not allowed.</p> 	Minor								

■ PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in contact with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcoholDo not scrub hard to avoid damaging the display surface.
- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solventsWipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.
- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.

ASI-T-430FA2AT/AH

- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability. To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.