



ASI-T-1040HA4LN/D

Item	Contents	Unit
Size	10.4	inch
Resolution	800(RGB) X 600	/
Interface	LVDS 6 bits	/
Color Depth	262K	/
Technology type	a-si TFT	/
Pixel pitch	0.264x0.264	mm
Pixel Configuration	R.G.B. Vertical Stripe	
Outline Dimension (W x H x D)	236.0x176.9x5.6	mm
Active Area	211.2x158.4	mm
Display Mode	TM with Normally White	/
Backlight Type	LED	/



Record of Revision

Date	Revision No.	Summary
2010-07-10	1.0	Rev 1.0 was issued



1. Scope

This data sheet is to introduce the specification of ASI-T-1040HA4LN/D Active matrix 262kcolor TFT module. It is composed of a color TFT-LCD panel, driver ICs, FPC and a backlight unit. The 10.4" display area contains 800(RGB) x 600 pixels.

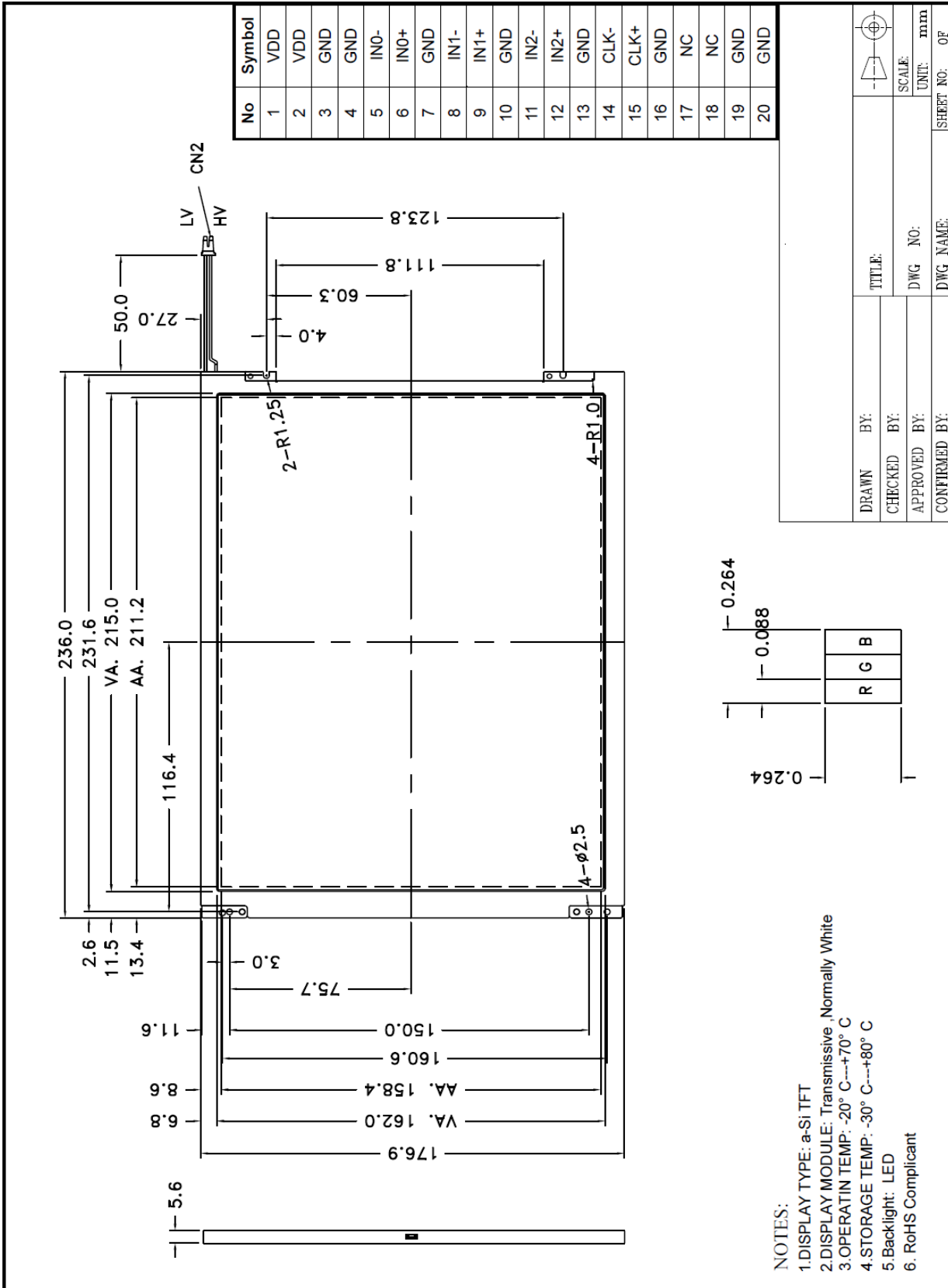
2. Application

Digital equipments which need color display outdoor, mobile navigator/video systems.

3. General Information

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4. Outline Drawing



5. Interface signals

No	Symbol	Description	Remarks
1	VDD	Power Supply	
2	VDD	Power Supply	
3	GND	Ground	
4	GND	Ground	
5	IN0-	LVDS receiver negative signal channel 0	
6	IN0+	LVDS receiver positive signal channel 0	
7	GND	Ground	
8	IN1-	LVDS receiver negative signal channel 1	
9	IN1+	LVDS receiver positive signal channel 1	
10	GND	Ground	
11	IN2-	LVDS receiver negative signal channel 2	
12	IN2+	LVDS receiver positive signal channel 2	
13	GND	Ground	
14	CLK-	LVDS receiver negative signal clock	
15	CLK+	LVDS receiver positive signal clock	
16	GND	Ground	
17	NC	No connection	
18	NC	No connection	
19	GND	Ground	
20	GND	Ground	

Corresponded connector: Hirose DF19K-20P-1H (56)

CN2 (LED connector)

	Symbol	Description	Wire Color
1	VL1	LED power supply(high voltage)	Red
2	VL2	LED power supply(GND)	White



6. Absolute maximum Ratings

6.1. Electrical Absolute max. ratings

Parameter	Symbol	MIN	MAX	Unit	Remark
Power Voltage	VDD	-0.3	5.0	V	
input voltage	VIN	-0.3	5.0	V	

6.2. Environment Conditions

Item	Symbol	MIN	MAX	Unit	Remark
Operating Temperature	TOPR	-20	70	°C	
Storage Temperature	TSTG	-30	80	°C	

6.3. LED Backlight Absolute max. ratings

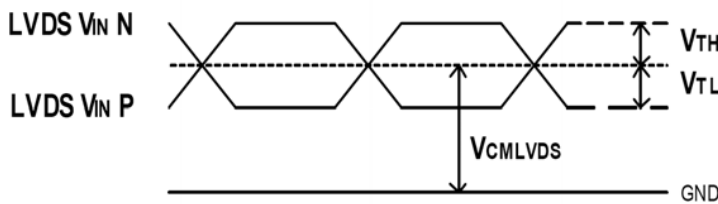
Item	Symbol	MIN	MAX	Unit	Remark
LED Forward Current	ILED	--	25	mA	For each LED
LED Reverse Voltage	VR	--	1.2	V	

7. Electrical Specifications

7.1 Electrical characteristics

GND=0V, Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
LVDS Differential input high threshold	VTH	-	-	100	mV	VCMLVDS=1.2V
LVDS Differential input low threshold	VTL	-100	-	-	mV	VCMLVDS=1.2V
Differential input voltage	VID	0.1	-	0.6	V	
LVDS input common mode voltage	VCMLVDS	VID /2	-	1.4-(VID /2)	V	
Input current	IIN	-10	-	10	μA	
Supply Voltage	VDD	3	3.3	3.6	V	
Common Electrode Driving Signal	VCOM	-	4.36	-	V	Note1
Sync Frequency	FVD	-	60	70	Hz	
VDD Power Consumption	IDD	-	TBD	380	mA	Note2

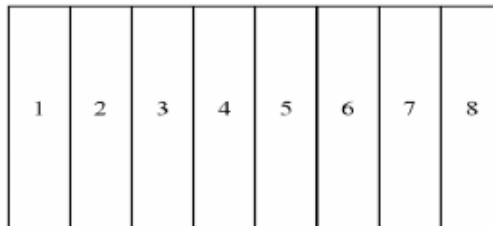


LVDS DC TIMING DIAGRAM

Note1: The value may be different for different LCM.

Note2: To test the current dissipation, using the “color bar” testing pattern shown as below:

1. White
2. Yellow
3. Cyan
4. Green
5. Magenta
6. Red
7. Blue
8. Black

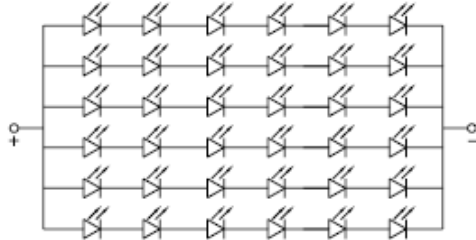


Current dissipation testing pattern

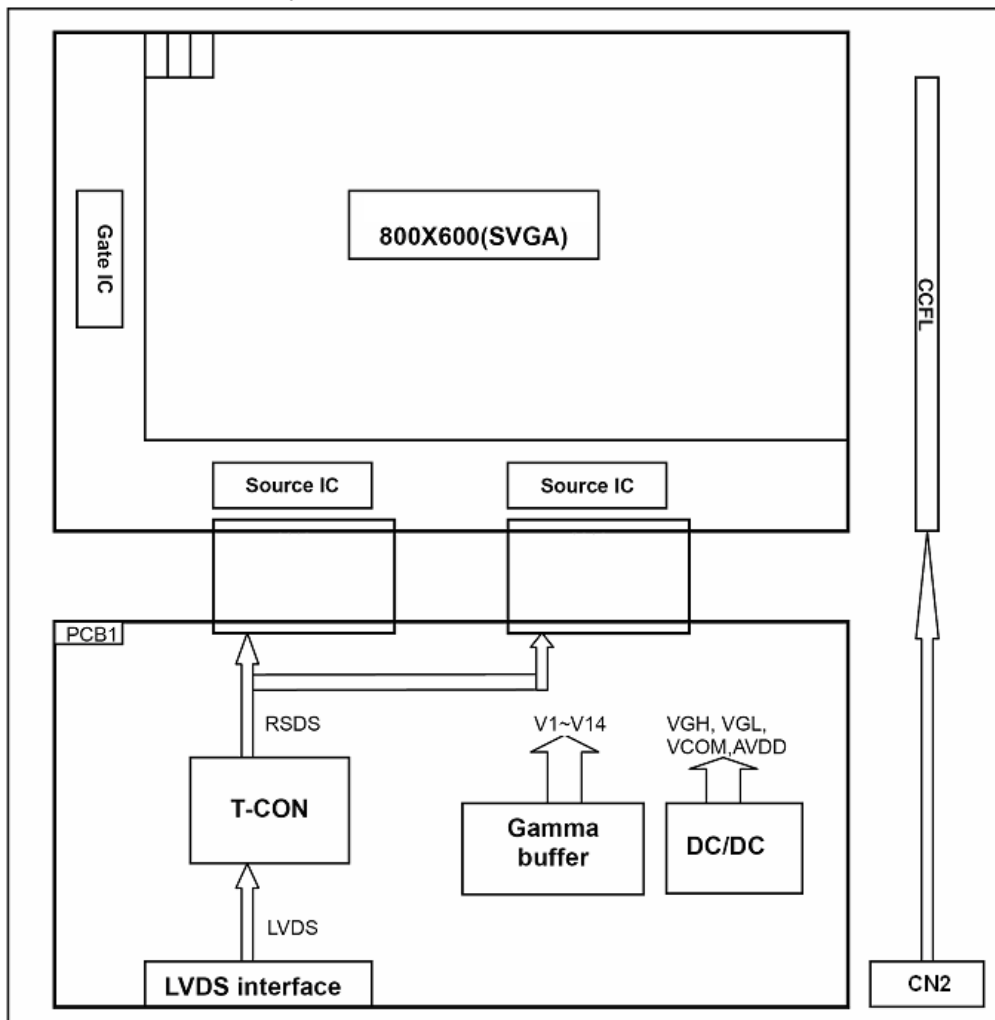
7.2 LED Backlight

Ta=25°C

Item	Symbol	MIN	TYP	MAX	Unit	Remark
Forward Current	IF	--	120	--	mA	6LEDs serial and 6parallel
Forward Voltage	VF	--	19.2	--	V	
Power Consumption	WBL	--	2.3	--	W	



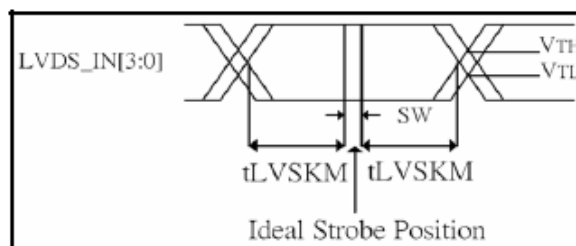
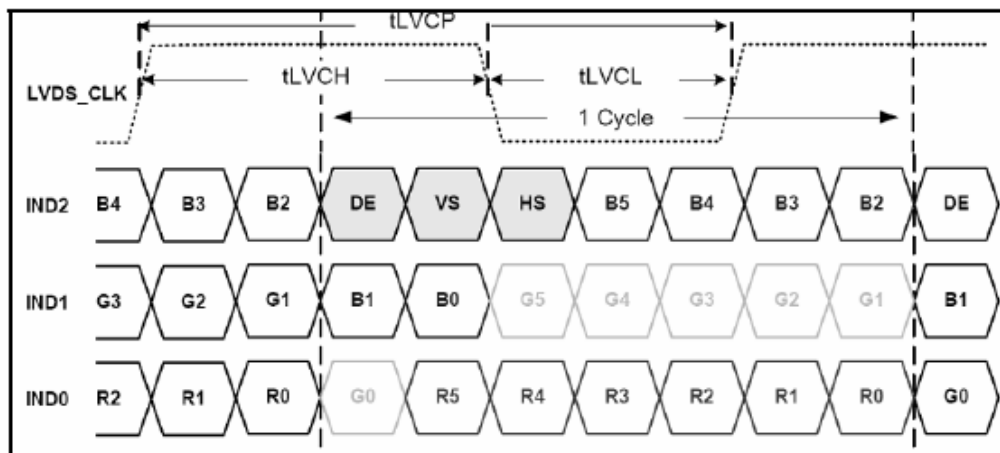
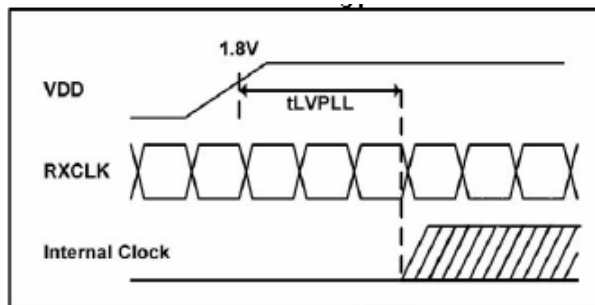
7.3 Schematic of LCD module system



8. Command/AC Timing

8.1 Timing Parameter

Item	Symbol	Rating			Unit
		MIN	TYP	MAX	
Clock period	tLVCP	20.0	25	31.25	ns
Clock high time	tLVCH	-	14.29	-	ns
Clock low time	tLVCL	-	10.71	-	ns
PLL wake-up time	tLVPLL	-	-	1	ms
Input skew margin (f=85MHz)	tLVSKM	400	-	-	ps

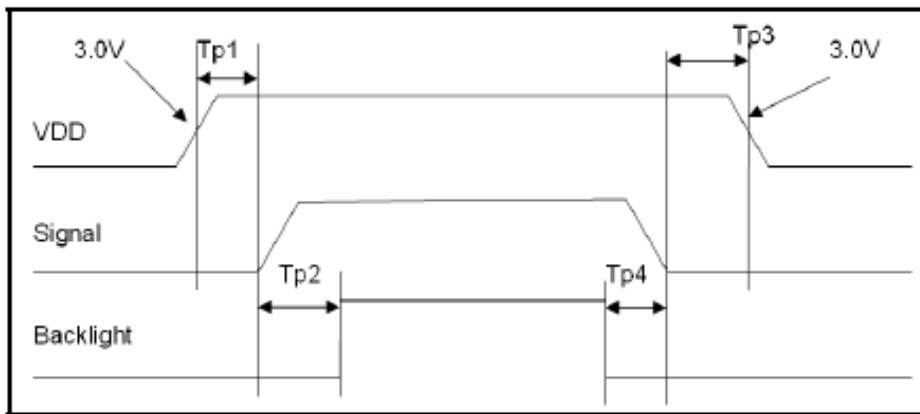


SW: Setup and Hold time

8.2 Power ON/Off Sequence

Parameter	Symbol	Rating			Unit
		MIN	TYP	MAX	
VDD 3.0V to signal starting	Tp1	0	-	30	ms
Signal starting to backlight on	Tp2	0	-	-	ms
Signal off to VDD 3.0V	Tp3	0	-	30	ms
Backlight off to signal off	Tp4	0	-	-	ms

Interface power on/off sequenc





9. Optical Specification

Backlight is ON

Ta=25°C

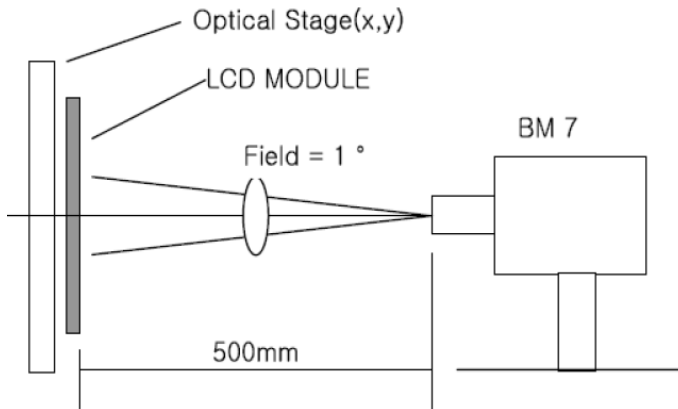
Item	Symbol	Condition	Min	Typ.	Max.	Unit	Remark
Contrast Ratio	CR	$\theta=0^\circ$	300	400	--		Note1 Note3
Response Time	Ton/ Toff	25°C	--	25	50	ms	Note1 Note4
View Angles	ΘT	$CR \geq 10$	35	45	--	Degree	Note 2
	ΘB		55	65	--		
	ΘL		55	65	--		
	ΘR		55	65	--		
Chromaticity	White	Brightness is on	x	TBD	--		Note5, Note1
			y	TBD	-		
Uniformity	U		--	80	-	%	Note1 Note6
NTSC	S		-	TBD	-	%	Note 5
Luminance	L		350	400	-	cd/m ²	Note1 Note7

NOTE: 1. The ambient temperature is 25±2 .humidity is 65±7%

Note 1: Definition of optical measurement system.

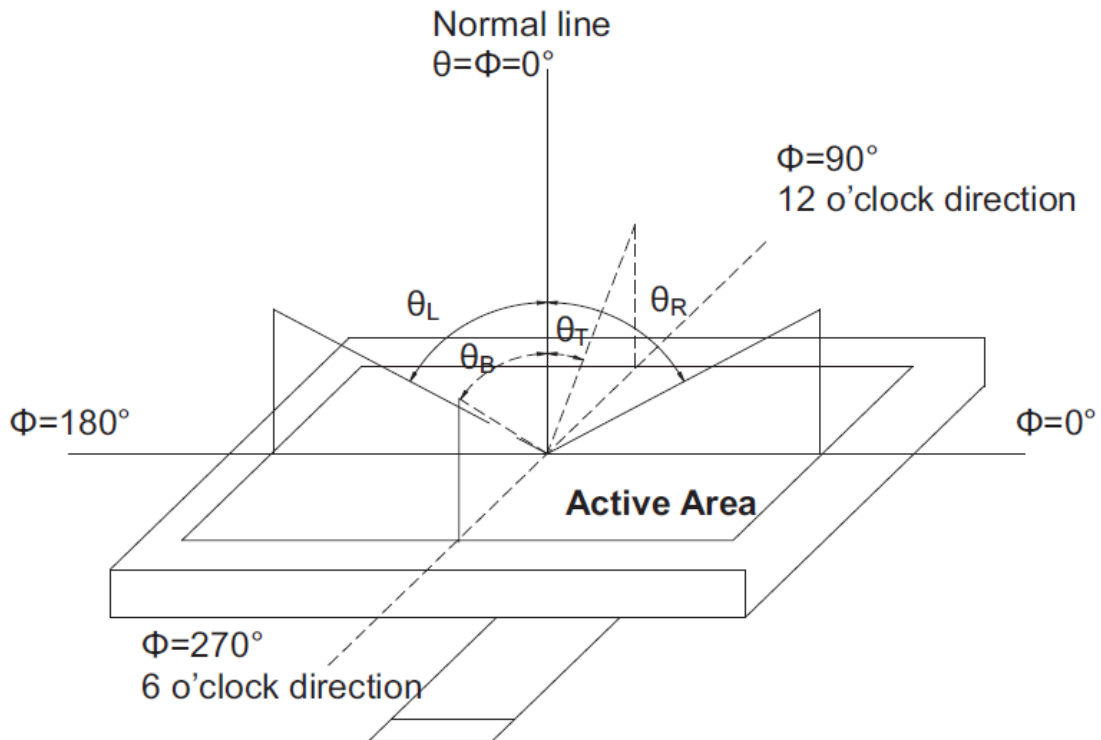
Temperature = 25°C(±3°C)

LED back-light: ON, Environment brightness < 150 lx



Note 2: Viewing angle range is defined as follow:

Viewing angle is measured at the center point of the LCD.

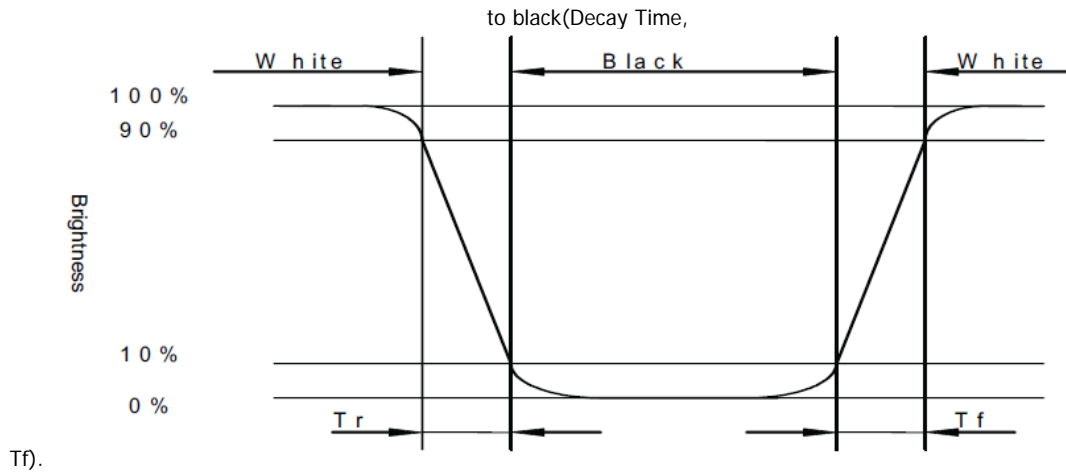


Note 3: Contrast ratio is defined as follow:

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

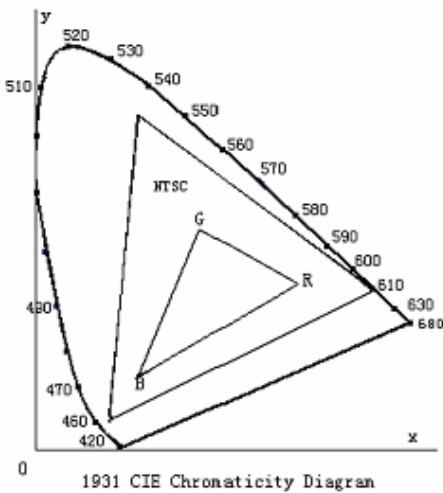
Note 4: Response time is defined as follow:

Response time is the time required for the display to transition from black to white (Rise Time, Tr) and from white



Note 5: Color chromaticity is defined as follow: (CIE1931)

Color coordinates measured at center point of LCD.



$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 6: Luminance Uniformity is defined as follow:

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

$$\text{Uniformity (U)} = \frac{\text{Minimum Luminance(brightness) in 9 points}}{\text{Maximum Luminance(brightness) in 9 points}}$$

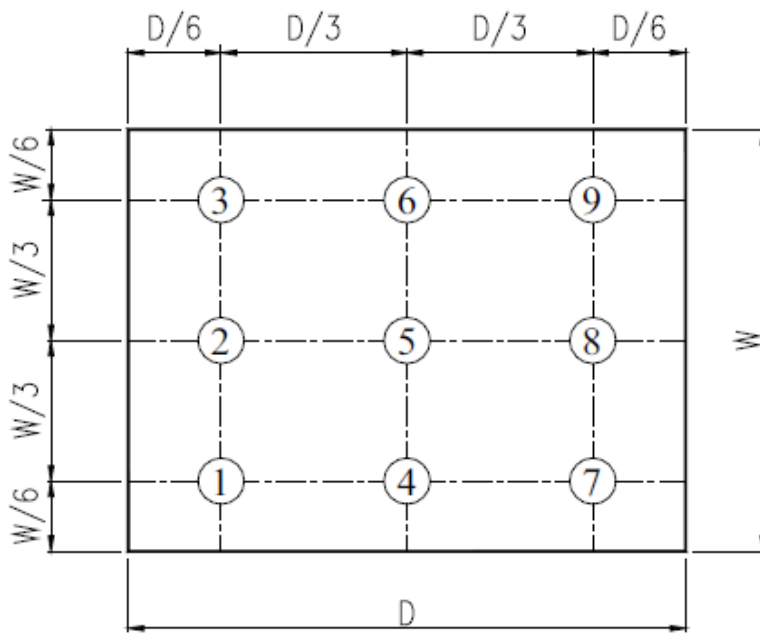


Fig. 2 Definition of uniformity

Note 7: Luminance is defined as follow:

Luminance is defined as the brightness of all pixels “White” at the center of display area on optimum contrast.

10. Environmental / Reliability Tests

No	Test Item	Condition	Judgment criteria
1	High Temp Operation	Ts=+70°C, 120hrs	Per table in below
2	Low Temp Operation	Ta=-20°C, 120hrs	Per table in below
3	High Temp Storage	Ta=+80°C, 120hrs	Per table in below
4	Low Temp Storage	Ta=-30°C, 120hrs	Per table in below
5	High Temp & High Humidity Storage	Ta=+60°C, 90% RH 120 hours	Per table in below (polarizer discoloration is excluded)
6	Thermal Shock (Non-operation)	-30°C 30 min~+80°C 30 min, Change time:5min, 10 Cycles	Per table in below
7	ESD (Operation)	C=150pF, R=330Ω , 5points/panel Air:±8KV, 5times; Contact:±4KV, 5 times;	Per table in below
8	Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z.	Per table in below
9	Shock (Non-operation)	60G 6ms, ±X,±Y,±Z 3times, for each direction	Per table in below
10	Package Drop Test	Height:80 cm, 1 corner, 3 edges, 6 surfaces	Per table in below

INSPECTION	CRITERION(after test)
Appearance	No Crack on the FPC, on the LCD Panel
Alignment of LCD Panel	No Bubbles in the LCD Panel No other Defects of Alignment in Active area
Electrical current	Within device specifications



Function / Display	No Broken Circuit, No Short Circuit or No Black line No Other Defects of Display
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11. Precautions for Use of LCD Modules

11.1 Safety

The liquid crystal in the LCD is poisonous. Do not put it in your mouth. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and water.

11.2 Handling

- A. The LCD and touch panel is made of plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- B. Do not handle the product by holding the flexible pattern portion in order to assure the reliability
- C. Transparency is an important factor for the touch panel. Please wear clear finger sacks, gloves and mask to protect the touch panel from finger print or stain and also hold the portion outside the view area when handling the touch panel.
- D. Provide a space so that the panel does not come into contact with other components.
- E. To protect the product from external force, put a covering lens (acrylic board or similar board) and keep an appropriate gap between them.
- F. Transparent electrodes may be disconnected if the panel is used under environmental conditions where dew condensation occurs.
- G. Property of semiconductor devices may be affected when they are exposed to light, possibly resulting in IC malfunctions.
- H. To prevent such IC malfunctions, your design and mounting layout shall be done in the way that the IC is not exposed to light in actual use.

11.3 Static Electricity

- A. Ground soldering iron tips, tools and testers when they are in operation.
- B. Ground your body when handling the products.
- C. Power on the LCD module before applying the voltage to the input terminals.
- D. Do not apply voltage which exceeds the absolute maximum rating.
- E. Store the products in an anti-electrostatic bag or container.

11.4 Storage

- A. Store the products in a dark place at $+25^{\circ}\text{C} \pm 10^{\circ}\text{C}$ with low humidity (40% RH to 60% RH). Don't expose to sunlight or fluorescent light.
- B. Storage in a clean environment, free from dust, active gas, and solvent.

11.5 Cleaning

- A. Do not wipe the touch panel with dry cloth, as it may cause scratch.
- B. Wipe off the stain on the product by using soft cloth moistened with ethanol. Do not allow ethanol to get in between the upper film and the bottom glass. It may cause peeling issue or defective operation. Do not use any organic solvent or detergent other than ethanol.

11.6 Cautions for installing and assembling

Bezel edge must be positioned in the area between the Active area and View area. The bezel may press the touch screen and cause activation if the edge touches the active area. A gap of approximately 0.5mm is needed between the bezel and the top electrode. It may cause unexpected activation if the gap is too narrow. There is a tolerance of 0.2 to 0.3mm for the outside dimensions of the touch panel and tail. A gap must be made to absorb the tolerance in the case and connector.

